

Figure 1

fig. 1



Figure 2

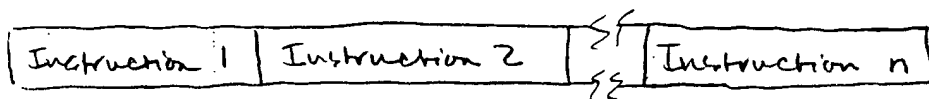


Figure 3

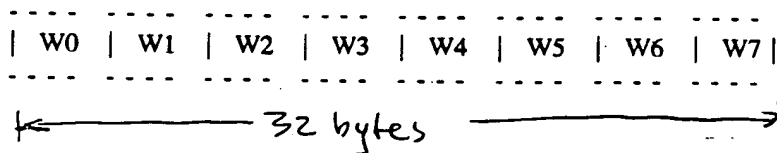


Figure 4

S	=	0	0	0	0	0	0	0
-----								
		W0	W1	W2	W3	W4	W5	W6
-----								
[ group 0 ]								

Figure 5a

S	=	0	0	1	1	1	0	0	0
-----									
		W0	W1	W2	W3	W4	W5	W6	W7
-----									
[ group 0 ]			[ group 1 ]			[ group 2 ]			

Figure 5b

S	=	0	1	0	1	0	1	0	1
-----									
		W0	W1	W2	W3	W4	W5	W6	W7
-----									
		[g0]	[g1]	[g2]	[g3]	[g4]	[g5]	[g6]	[g7]

Figure 5c

63	59	56					0
-----							
P	S	INSTRUCTION					
-----							
4	3					57	

Figure 6

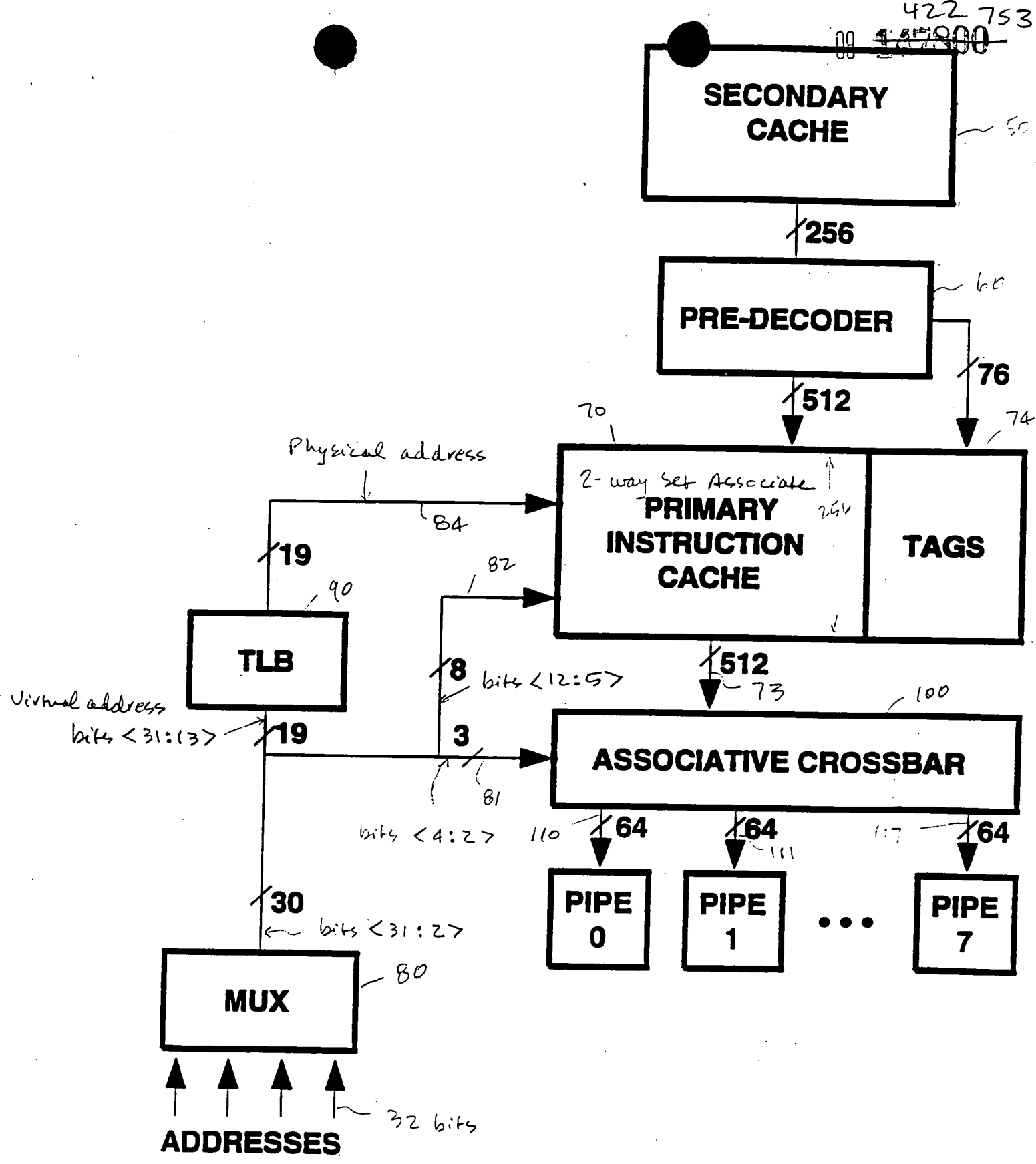
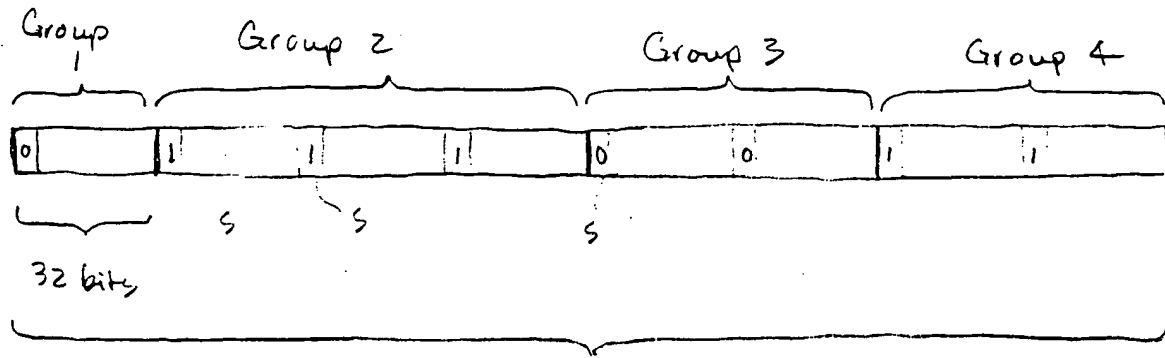
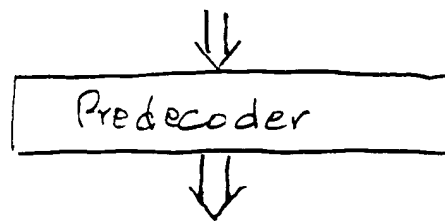


Figure 8

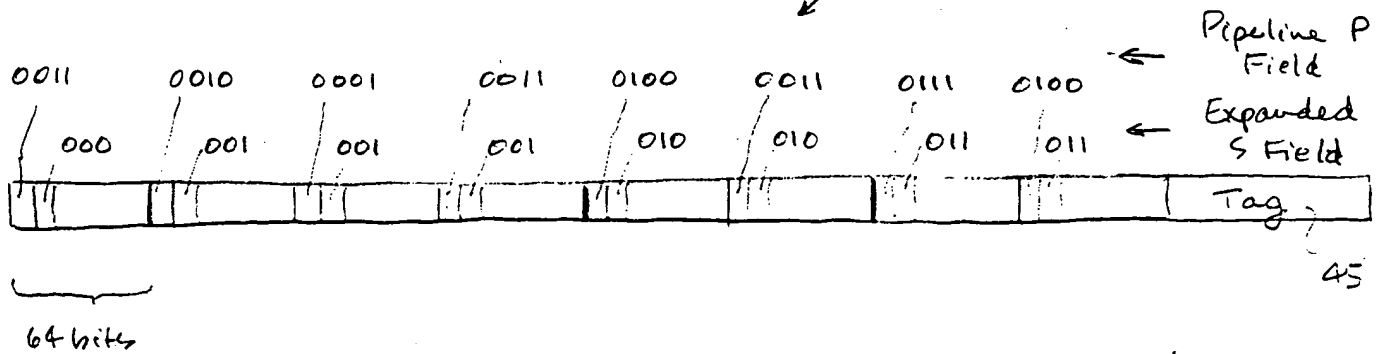


$$8 \times 32 = 256 \text{ bits}$$



Frame in Memory

Frame in I-cache



512 bits

bits

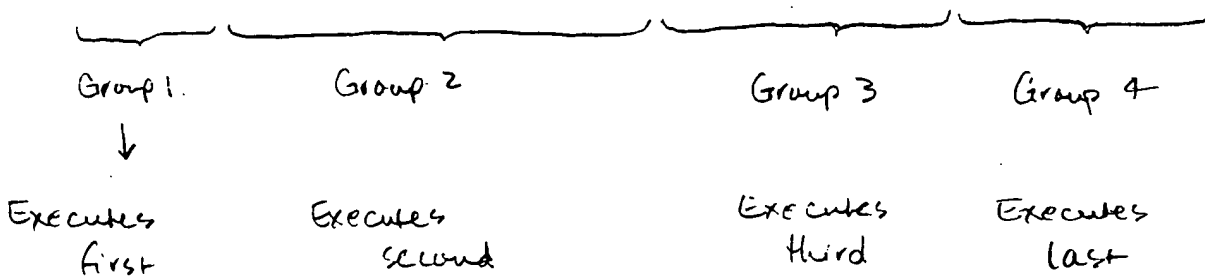


Figure 7

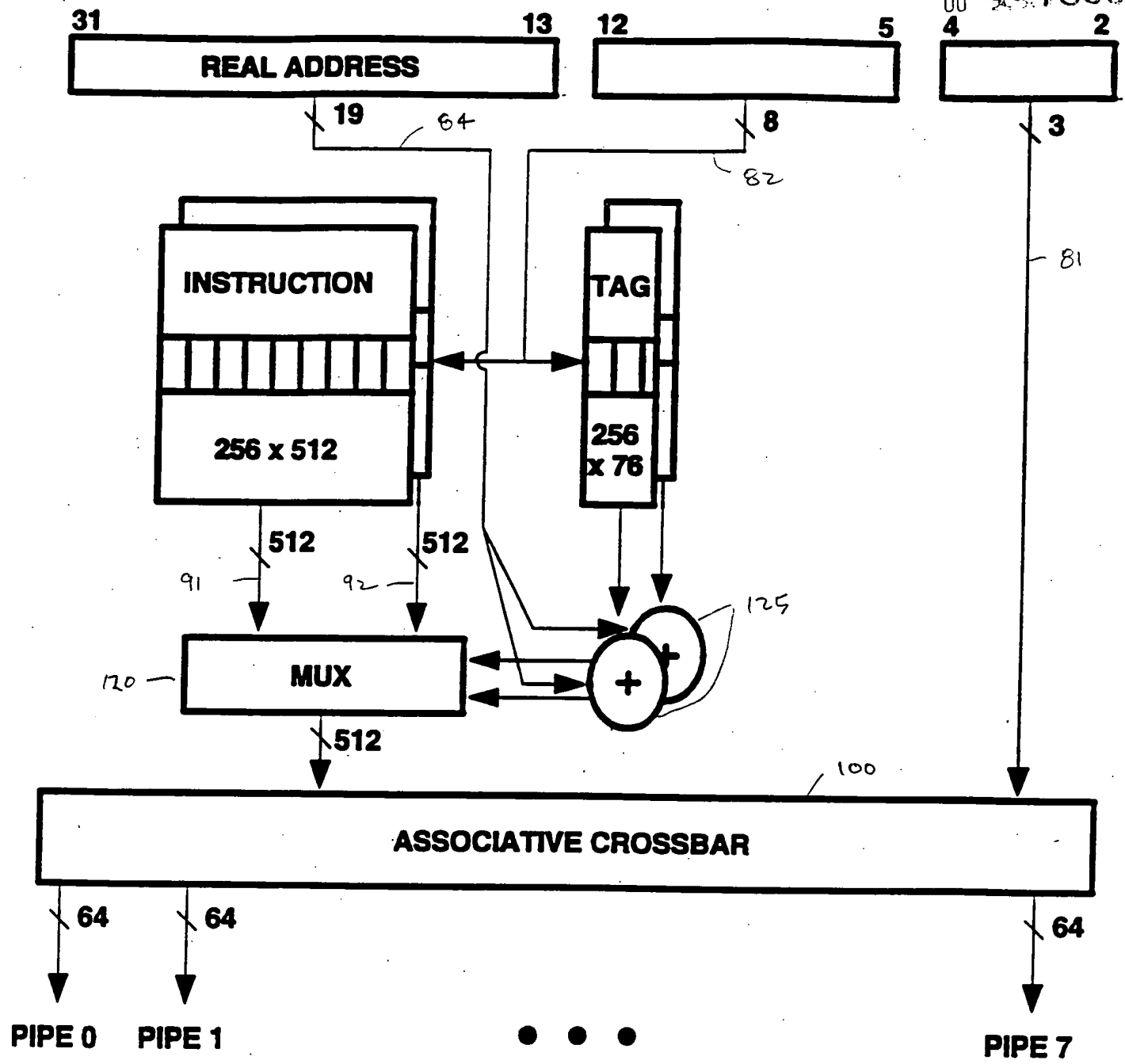


Figure 9

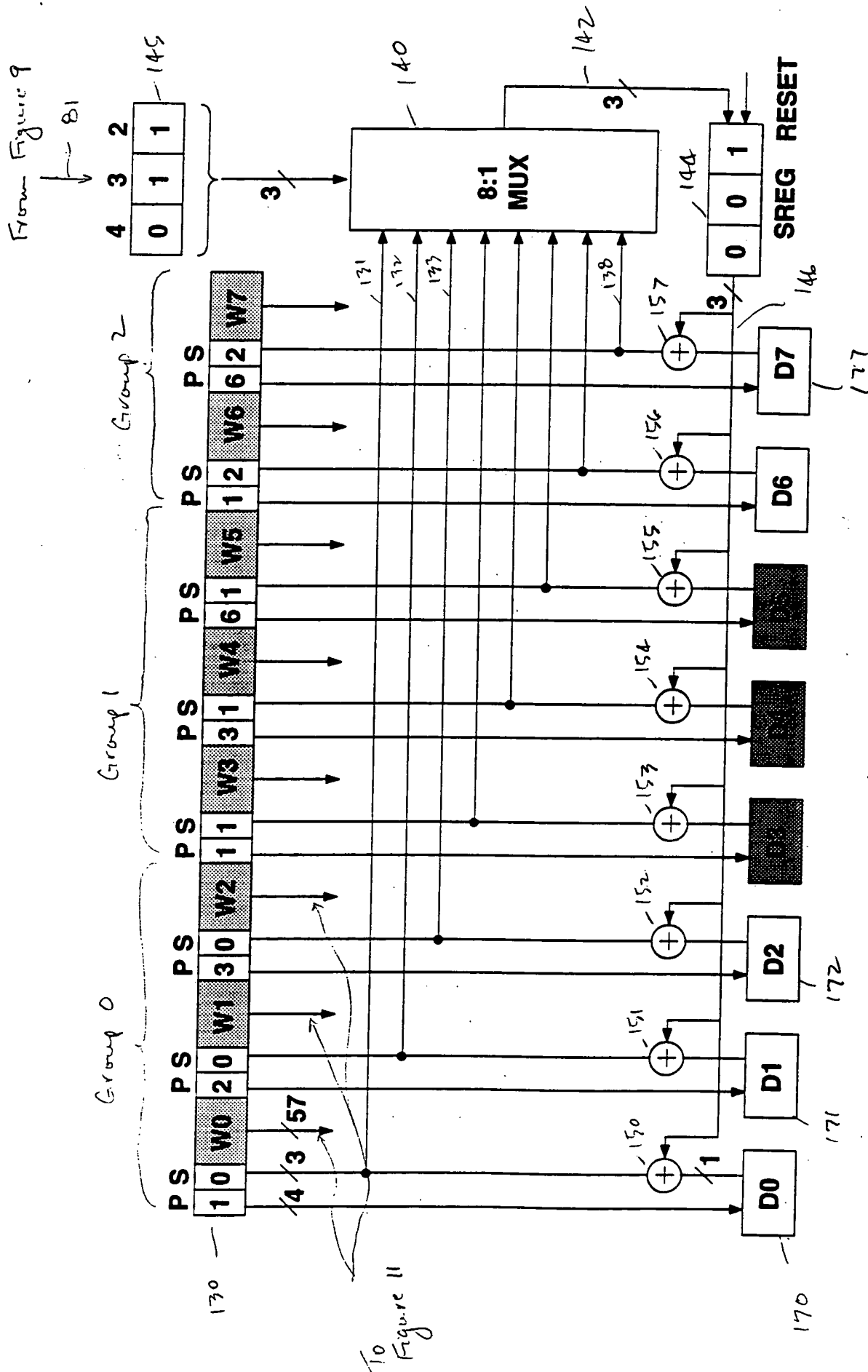
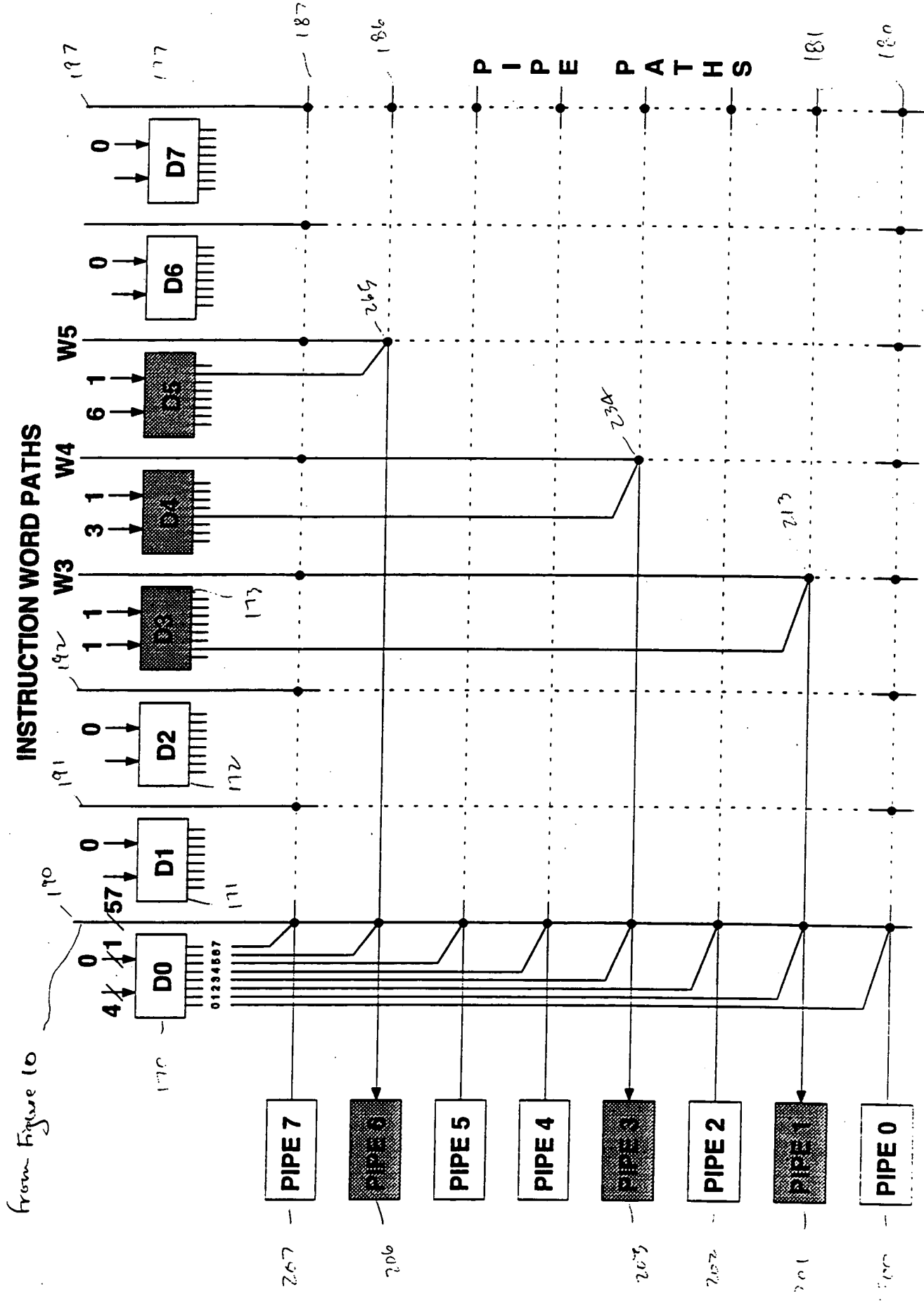


Figure 10



from Figure 10

Figure 11



ALU	ALU	LOAD	STORE	LOAD	FP	FP	Br
0	0	0	0	1	1	1	2

Group Tag

Figure 12

CLOCK 1 --	ALU	ALU	LOAD	STORE
CLOCK 2 --	LOAD	FP	FP	
CLOCK 3 --	BRANCH			

Figure 13

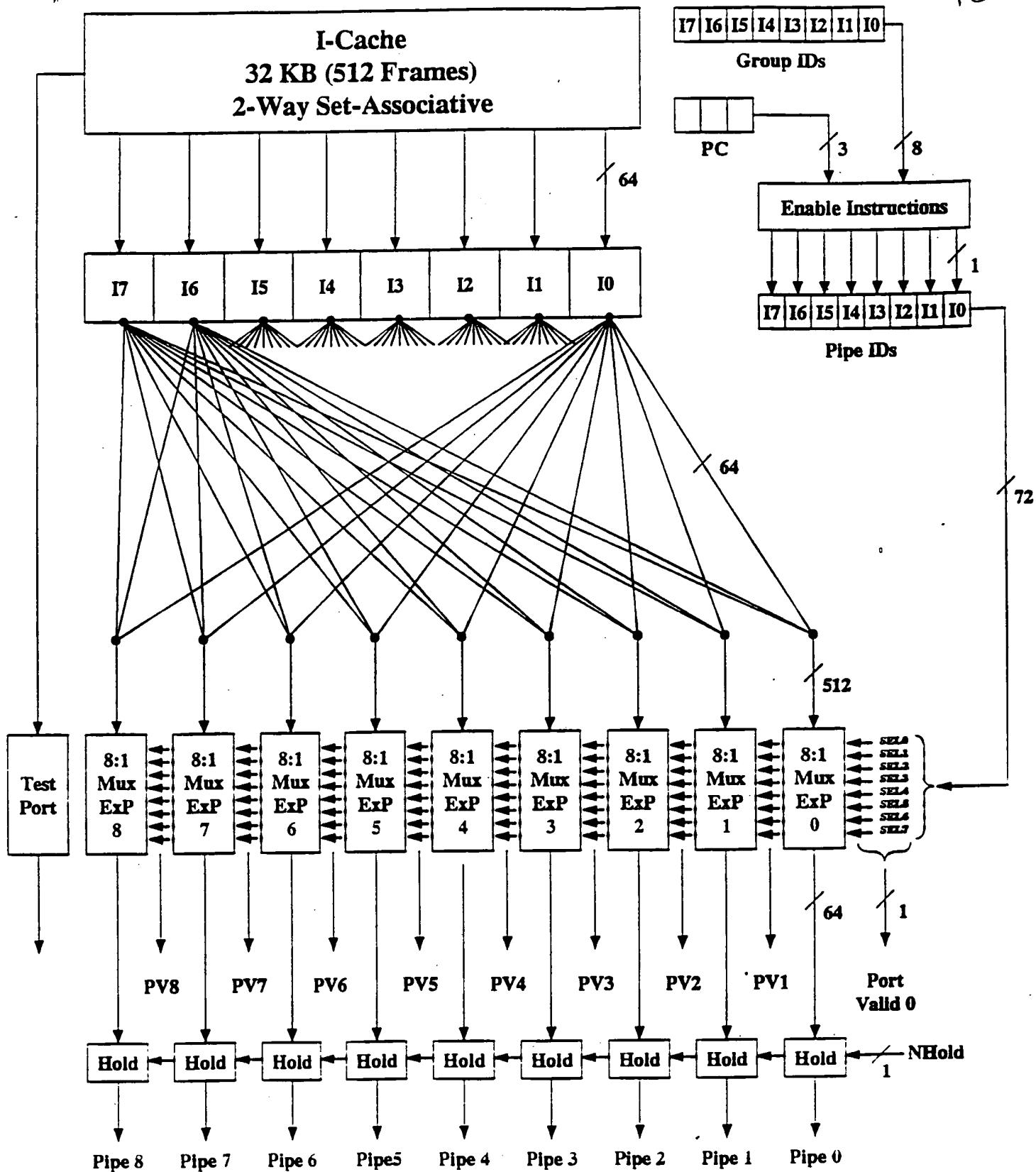


Figure 14-

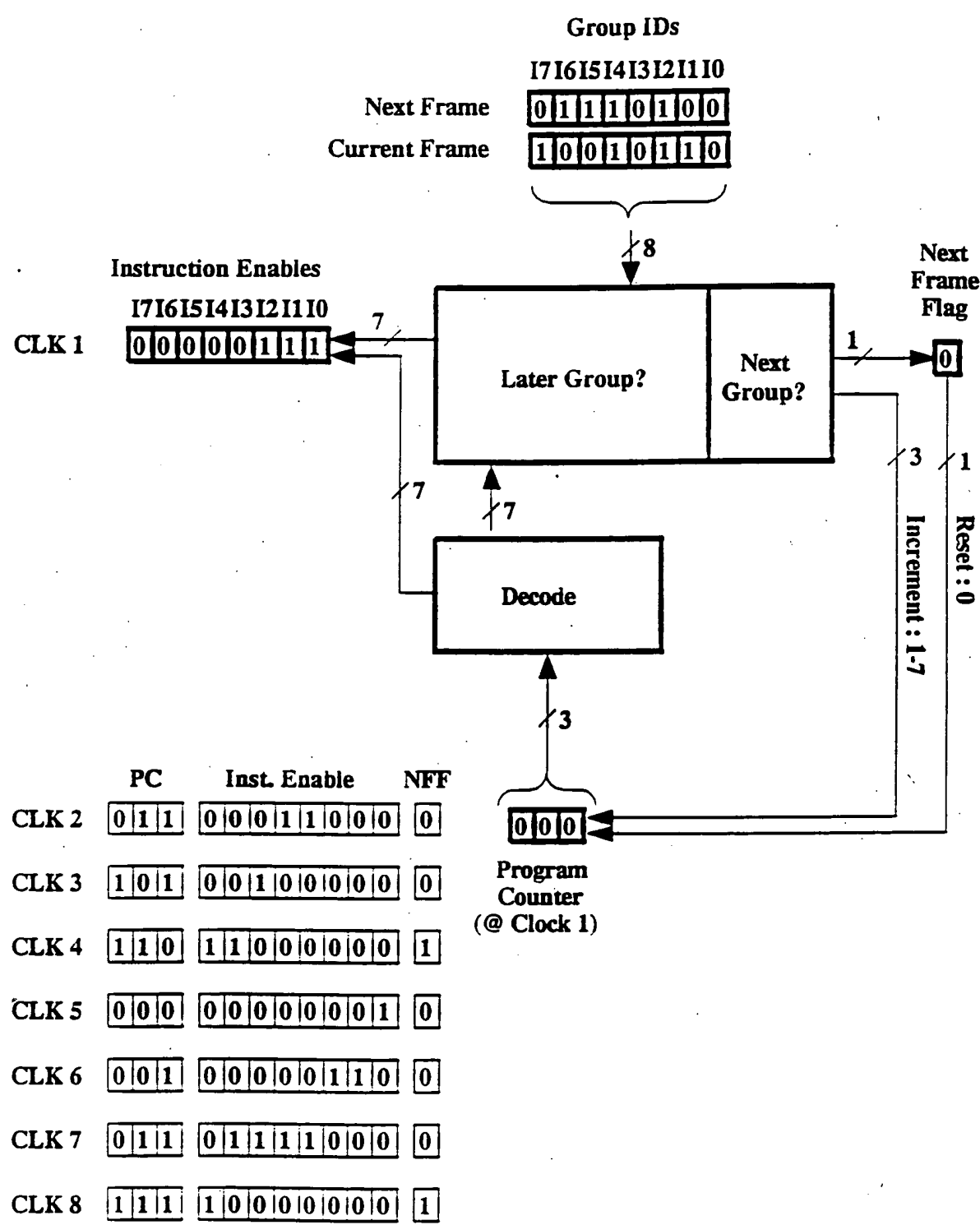


Figure 15